



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Austin H. Lesea  
Assignee: Xilinx, Inc.  
Title: "INTEGRATED TESTING OF SERIALIZER/DESERIALIZER IN  
FPGA"  
Serial No.: 09/912,683 File Date: July 24, 2001  
Examiner: Cynthia H. Britt Art Unit: 2133  
Docket No.: X-895 US Conf. No.: 7141

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AMENDMENT UNDER 37 CFR § 1.111

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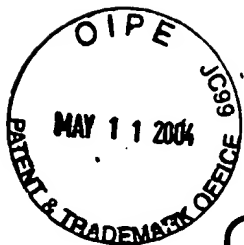
In response to the first Office Action mailed from the Patent Office on February 9, 2004, please replace/substitute the following paragraph(s)/section(s)/claim(s) as indicated.

**Amendments to the Drawings** begin on page 2.

**Amendments to the Specification** begin on page 3.

**Amendments to the Claims** begin on page 4.

**Remarks/Arguments** begin on page 9.



Modified 02-03

PTO/SB/21 (01-03)  
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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application / Conf. No.	09/912,683 / 7141
	Filing Date	July 24, 2001
	First Named Inventor	Austin H. Lesea
	Examiner Name	Cynthia H. Britt
	Art Unit	2133
	Patent No.	
Express Mail Receipt No.		
Total Number of Pages in This Submission		Attorney Docket Number X-895 US

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<input checked="" type="checkbox"/> Amendment / Reply	<input type="checkbox"/> Declaration / Oath	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Preliminary Amendment	<input checked="" type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
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